

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,053,432 B2  
APPLICATION NO. : 09/882534  
DATED : May 30, 2006  
INVENTOR(S) : Garo J. Derderian et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page and Col. 1, please delete the title "ENHANCED SURFACE AREA CAPACITOR FABRICATION METHODS" and insert --CAPACITOR FABRICATION METHODS AND CAPACITOR CONSTRUCTIONS--

Title Page, References Cited, U.S. PATENT DOCUMENTS  
Replace "6,033,987"  
With --6,033,967--

Page 2, U.S. PATENT DOCUMENTS, please insert  
--6,281,543 08/2001 Al-Shareef et al.-- after "6,281,142"  
--6,309,923 10/2001 Tseng-- after "6,307,730"

Column 7, claim 1, line 20  
Replace "and the second electrode, the barrier containing  $\text{Al}_2\text{O}_2\text{O}_3$ ,"  
With --and the second electrode, the barrier layer containing  $\text{Al}_2\text{O}_3$ --

Column 8, claim 7, line 3  
Replace "electrode, the barrier layer containing  $\text{Al}_2\text{O}_2\text{O}_3$ , having"  
With --electrode, the barrier layer containing  $\text{Al}_2\text{O}_3$ , having--

Column 8, claim 11, line 24  
Replace "layer containing  $\text{Al}_2\text{O}_2\text{O}_3$ , having a thickness of less"  
With --layer containing  $\text{Al}_2\text{O}_3$ , and having a thickness of less--

Signed and Sealed this

Twentieth Day of May, 2008



JON W. DUDAS  
*Director of the United States Patent and Trademark Office*